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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/020,332	12/12/2001	Seong-Joong Kim	678-735(P10196)	1734
28249	7590	02/22/2006	EXAMINER	
DILWORTH & BARRESE, LLP 333 EARLE OVINGTON BLVD. UNIONDALE, NY 11553			RAMAKRISHNAIAH, MELUR	
			ART UNIT	PAPER NUMBER
			2643	

DATE MAILED: 02/22/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/020,332

Applicant(s)

KIM, SEONG-JOONG

Examiner

Melur Ramakrishnaiah

Art Unit

2643

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 December 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 2 and 5-7 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 3-21-02, 7-11-05, 12-28-05
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 5-7 are rejected under 35 U.S.C 102(e) as being anticipated by Adachi (US 2002/0155822A1, filed 8-24-2001, hereinafter Adachi).

Regarding claim 1, Adachi discloses a direct conversion receiver for substantially removing DC offset signals in a mobile communication terminal, the receiver comprising: converting means (108-109, fig. 2) for down converting a modulated signal received from an antenna (101, fig. 2), detecting means (113/114, fig. 2) for detecting a difference between two DC offset signal components, and adjusting means in (113/114, fig. 2) for substantially reducing the difference (fig. 2, paragraphs: 0031 – 0054).

Regarding claim 5, Adachi discloses a method for substantially removing DC offset signals utilizing a direct-conversion receiver, the method comprising the steps of: down converting a modulated signal receiver from an antenna, detecting a difference between the DC offset signal components from balanced mixers (108-111, fig. 2) and adjusting the difference between detected DC offset signals components to minimize the difference (fig. 2, paragraphs: 0031 – 0054).

Regarding claim 6-7, Adachi further teaches the following: outputting a value to minimize the difference (paragraph: 0054), mixing a signal provided by low noise amplifier (reads on 103, fig. 2) with two I/Q components generated in local oscillator (112, fig. 2), respectively, converting the mixed signal into a base band signal, and amplifying (by 117/118, fig. 2) the base band signal based upon a value generated by an adjustment means (109/114, fig. 2) of the direct conversion receiver (fig. 2, paragraphs: 0031 – 0054).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Adachi in view of Kataoka et al. (JP410247953A, hereinafter Kataoka).

Regarding claim 2, Adachi teaches the following: at least one mixer (108, fig. 2) for mixing the signal provided from a low amplifier (reads on 103, fig. 2) with two I/Q components that are separated by 180 degree in phase, at least one low pass filter (19/20, fig. 1) for eliminating spurious signals generated in the mixers, and at least one compensation amplifier (117/118, fig. 2) for compensating the DC offset signal, wherein the first amplifier has a fixed gain (fig. 2, paragraphs: 0031 – 0054).

Adachi differs from claim 2 in that he does not teach the following: second amplifier has a variable gain.

However, Kataoka discloses receiver which teaches the following: amplifier (8, fig. 1) has a variable gain.

Thus, it would have been obvious to one of ordinary skill in the art at the time invention was made to modify Adachi's system to provide for the following: second amplifier has a variable gain as this arrangement would provide means for fine tuning DC offset cancellation in the receiver as taught by Kataoka.

5. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Adachi in view of Mitama (EP 0863606A1).

Adachi differs from claim 4 in that he does not teach the following: a switching means for connecting the converting means to detecting means.

However, Mitama discloses direct conversion receiver capable of canceling DC offset voltages which teaches the following: a switching means (20) for connecting the converting means to detecting means (fig. 1, see abstract).

Thus, it would have been obvious to one of ordinary skill in the art at the time invention was made to modify Adachi's system to provide for the following: a switching means for connecting the converting means to detecting means as this arrangement would provide alternative method for implementing DC offset cancellation in a receiver as taught by Mitama.

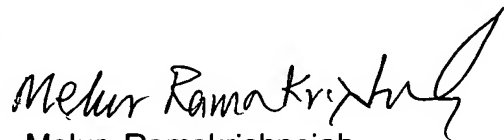
6. Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Melur Ramakrishnaiah whose telephone number is (703) 305-1461. The examiner can normally be reached on M-F 6:30-4:00; every other F Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Curtis Kuntz can be reached on (703)305-4708. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Melur Ramakrishnaiah
Primary Examiner
Art Unit 2643